

IN THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1 – 12. (Cancelled)

13. (NEW) A semiconductor integrated circuit device comprising:

a first RAM for inputting and outputting data between a bus and itself;

a second RAM for inputting and outputting data between the bus and itself;

a secret key holder for holding a secret key;

a bus port for controlling access from outside to the bus;

a CPU for storing an encrypted program in the first RAM via the bus port, decrypting the encrypted program by using a decryption program and the secret key, and executing the decrypted program; and

a controller for disabling access from the outside to the first RAM and the second RAM and enabling access from the CPU to the first RAM and the second RAM when the encrypted program is stored in the first RAM and when the decrypted program in the second RAM is executed.

14. (NEW) A semiconductor integrated circuit device comprising:

a first RAM for inputting and outputting data between a bus and itself;

a second RAM for inputting and outputting data between the bus and itself;

a secret key holder for holding a secret key;

a bus port for controlling access from outside to the bus;

a CPU for storing an encrypted program in the first RAM via the bus port, decrypting the encrypted data by using a decryption program and the secret key, and using the decrypted data;

and

a controller for disabling access from the outside to the first RAM and the second RAM and enabling access from the CPU to the first RAM and the second RAM when the encrypted data is stored in the first RAM and when the decrypted data is stored in the second RAM.